



NEWS RELEASE

Credo's Toucan PCIe® Retimer Achieves PCI Express® Compliance and Joins PCI Express® 6.x Integrators List

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SAN JOSE, Calif.--(BUSINESS WIRE)-- **Credo Technology Group Holding Ltd** (Credo) (NASDAQ: CRDO), an innovator in providing connectivity at scale through fast, reliable, and energy-efficient system solutions, today announced that its Toucan Gen6x16 PCI Express® (PCIe®) retimer has successfully achieved PCI-SIG® 6.x retimer compliance at 64.0 GT/s and is now listed on the PCI-SIG Integrators List.

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Key points:

- Credo's Toucan Gen6x16 PCIe retimer successfully completed PCI-SIG 6.x retimer compliance testing at the recent PCI-SIG Compliance Workshop which concluded on July 31.
- Toucan is now listed on the PCI-SIG Integrators List, giving customers added confidence in standards-based PCIe 6.0 deployment.
- The compliance milestone validates Toucan across electrical compliance, interoperability, and expanded PCIe 6.0 retimer protocol requirements.
- Toucan combines PCIe 6.0 performance with DSP-based SerDes, low-power operation and deep diagnostics through PILOT software.

Purpose-Built for Next-Gen Platforms

Toucan retimers address integration challenges in AI, HPC, and accelerated compute environments with:

- PCIe 6.0 technology-compatible interoperability
- Low-power design for dense accelerator platforms
- Advanced debug and diagnostics through award-winning PILOT tool
- Architecture aligned with PCIe 7.0 technology roadmaps

“As AI infrastructure scales from individual servers to rack-scale and cluster-scale architectures, the role of PCIe architecture connectivity becomes increasingly important,” said Vishal Shah, Vice President of Product at Credo. “Achieving PCIe 6.0 specification compliance and inclusion on the PCI-SIG Integrators List validates the ability of Credo’s PCIe retimer to deliver the signal integrity, interoperability, diagnostics, and power efficiency required by next-generation AI systems. This milestone gives customers confidence that Toucan is ready for production deployment in the most demanding accelerated computing environments.”

“We congratulate Credo on achieving PCI-SIG 6.x compliance for its Toucan retimers,” said Al Yanes, PCI-SIG President and Chairperson. “Industry collaboration and adherence to open standards are essential to advancing the PCI Express technology ecosystem.”

Key Customer Benefits

- PCIe 6.0 specification x16 readiness for AI, HPC, cloud, and accelerated compute platforms.
- Reduced integration and qualification risk through standards-based validation.
- Faster platform bring-up and root-cause debug using PILOT diagnostics and telemetry.
- Reliable operation across complex high-speed topologies and high-loss channels.
- Low-power design for dense accelerator and server environments.

The successful completion of PCI-SIG 6.x retimer testing underscores Credo's continued execution in high-speed connectivity and reinforces Toucan as a key building block for reliable PCIe 6.0 technology deployment in modern AI infrastructure.

To learn more about Credo’s PCIe retimer solutions, visit <https://credosemi.com/products/pcie/>.

About Credo

Credo’s mission is to transform connectivity at scale through fast, reliable, and energy-efficient system solutions. Our high-speed copper and optical interconnect technologies deliver industry-leading power and performance from chip to cluster to meet the ever-expanding data infrastructure demands of AI.

Our vertically integrated connectivity portfolio is comprised of our flagship purple ZeroFlap (ZF) Active Electrical Cables (AECs) and ZF optical transceivers; optical components including silicon photonics-based photonic integrated

circuits (SiPho PICs) and DSPs; OmniConnect AI memory and chip-to-chip interconnect; and retimers for Ethernet and PCIe—supported by our PILOT diagnostic and analytics software platform. Credo innovations enable our customers to connect the systems that connect the world.

For more information, please visit <https://www.credosemi.com>. Follow Credo on [LinkedIn](#).

About PCI-SIG

PCI-SIG is the consortium that owns and manages PCI specifications as open industry standards. The organization defines industry standard I/O (input/output) specifications consistent with the needs of its members. Currently, PCI-SIG is comprised of over 1,000 companies. To join PCI-SIG, and for a list of the Board of Directors, visit <https://www.pcisig.com/>.

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Media Contact:

Kristin Hehir

kristin.hehir@credosemi.com

Investor Contact:

Dan O'Neil

dan.oneil@credosemi.com

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