NEWS RELEASE

Credo Unveils Industry's First Memory Fanout Gearbox for Scalable, High-Bandwidth AI Inference

2025-11-03

Weaver—the First Product in Credo's OmniConnect Family—Overcomes Memory Bottlenecks in AI Inference Workloads to Boost Memory Density and Throughput

SAN JOSE, Calif.--(BUSINESS WIRE)-- **Credo Technology Group Holding Ltd** (Credo) (NASDAQ: CRDO), an innovator in providing secure, high-speed connectivity solutions that deliver improved reliability and energy efficiency, today announced Weaver, a memory fanout gearbox that significantly boosts memory bandwidth and memory density to optimize computing efficiency of AI accelerators or xPUs. Credo's OmniConnect family, in which Weaver is the first member, encompasses solutions designed to address scale up and scale out concerns for AI buildouts. Weaver is engineered to overcome the memory bottlenecks in AI inference workloads, delivering unparalleled scalability, bandwidth, and efficiency for next-generation data center and AI applications.

Credo introduces Weaver, a memory fanout gearbox engineered to overcome the memory bottlenecks in AI inference workloads. The first product in Credo's OmniConnect family, Weaver boosts memory bandwidth and throughput to deliver scalability and efficiency for next-generation data center and AI applications.

AI inference workloads are increasingly limited by memory quantity and throughput rather than compute power. Traditional memory solutions, such as LPDDR5X/GDDRX, face constraints in bandwidth, density, and power consumption, restricting system performance and scalability. High Bandwidth Memory (HBM) suffers from very high cost, limited availability, and density issues. Weaver leverages advanced 112G very short reach (VSR) SerDes and Credo's proprietary design to boost I/O density by up to 10x, enabling up to 6.4TB of memory and 16TB/s bandwidth using LPDDR5X—far surpassing conventional architectures.

“Weaver is designed to deliver the flexibility and scalability required for future AI inference systems,” said Don Barnetson, Senior Vice President, Product at Credo. “This innovation empowers our partners to optimize memory provisioning, reduce costs, and accelerate deployment of advanced AI workloads.”

“The future of AI acceleration requires efficiency at all levels and innovative technology to process extremely large workloads,” said Mitesh Agrawal, CEO of Positron. “Credo’s Weaver is instrumental in helping us solve our toughest memory challenges, enabling us to deliver the high-performance compute power for our next generation of AI inference servers.”

Weaver supports flexible DRAM packaging and late binding, allowing system integrators to tailor memory configurations for evolving model requirements. The technology is ready for seamless migration to next-gen memory protocol, ensuring long-term value and compatibility as the ecosystem advances. Weaver also integrates robust telemetry and diagnostics for enhanced reliability and uptime.

To discover more about Weaver and OmniConnect, please register for Credo’s upcoming webinar on November 10 at 8:00 am PT/11:00 am ET, “Breaking the Memory Wall: Scaling AI Inference with Innovative Memory Fanout Architecture,” at the link found **here**.

To learn more about the Credo products in this release, go to the product pages linked **here**.

Product Availability

The Credo OmniConnect 112G VSR interface is available for design-in now. The Credo Weaver memory fanout gearbox is scheduled to be available in 2H of 2026 from Credo.

About Credo

Credo’s mission is to redefine high-speed connectivity by delivering breakthrough solutions that enable the next generation of AI-driven applications. We are committed to enabling faster, more reliable, more energy-efficient, and scalable solutions that support the ever-expanding demands of AI, cloud computing, and hyperscale networks. Our innovations ease system bandwidth bottlenecks while simultaneously improving on power, security, and reliability. Our connectivity solutions are optimized for optical and electrical Ethernet applications, including the emerging 100G (or Gigabits per second), 200G, 400G, 800G and the emerging 1.6T (or Terabits per second) port markets. Credo products are based on our proprietary Serializer/Deserializer (SerDes) and Digital Signal Processor (DSP) technologies. Our product families include Integrated Circuits (ICs) for the optical and line card markets, Active Electrical Cables (AECs) and SerDes Chiplets. Our intellectual property (IP) solutions consist primarily of SerDes IP licensing.

For more information, please visit **https://www.credosemi.com**. Follow Credo on **LinkedIn**.

Media Contact:

Diane Vanasse

diane.vanasse@credosemi.com

Investor Contact:

Dan O'Neil

dan.oneil@credosemi.com

Source: Credo