



NEWS RELEASE

Credo Launches 224G PAM4 SerDes IP on TSMC N3 Process Technology

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Accelerates Pathway to Ultra High-Speed 1.6Tbps Bandwidth for Build Out of the Next Generation of Cloud Computing, AI, and Hyperscale Networks

SAN JOSE, Calif.--(BUSINESS WIRE)-- **Credo Technology Group Holding Ltd** (Credo) (NASDAQ: CRDO), an innovator in providing secure, high-speed connectivity solutions that deliver improved reliability and energy efficiency, today introduced its 224G PAM4 SerDes Intellectual Property (IP) on TSMC's industry-leading N3 technology.

Key points:

- The new 224G PAM4 IP offering brings Credo's high-performance, power-efficient SerDes technologies with fabrication on an industry-leading advanced process technology from TSMC to provide the foundation for the design of high-bandwidth, low-latency 1.6Tbps port connectivity solutions.
- The 224G per lane data transmission provides the networking backbone which is critical for the deployment of next-generation AI, cloud computing and hyperscale applications.

"Credo's 224G PAM4 IP doubles the data rate of our 112G PAM4 IP predecessor to help our customers ramp quickly and efficiently design cutting-edge connectivity solutions at 1.6T network speeds," said Jeff Twombly, Vice President of Business Development at Credo. "This newest addition to our SerDes IP portfolio is a powerful building block that provides high-performance and ultra-low-power networking connectivity to scale massive, compute-intensive AI workloads."

The 1.6Tbps networking market is projected to reach \$13B by 2028 with ramp beginning at the end of this year,

according to Alan Weckel, Founder and Analyst, 650 Group.

Credo's comprehensive SerDes IP family includes a wide range of signaling options that span 28G to 224G and reach options that include long reach plus (LR+), long reach (LR), medium reach (MR) and very short reach plus (VSR). Credo's SerDes technology enables silicon solution providers and OEMs to manufacture custom chip solutions which address new market opportunities, while delivering on critical performance and low-power system-level requirements. All Credo IP solutions are supported with evaluation boards, simulation models, characterization reports, reliability reports, design libraries and a complete set of supporting documentation. Customers interested in this new IP should contact **sales@credosemi.com**.

About Credo

Credo's mission is to redefine high-speed connectivity by delivering breakthrough solutions that enable the next generation of AI-driven applications. We are committed to enabling faster, more reliable, more energy-efficient, and scalable solutions that support the ever-expanding demands of AI, cloud computing, and hyperscale networks. Our innovations ease system bandwidth bottlenecks while simultaneously improving on power, security, and reliability. Our connectivity solutions are optimized for optical and electrical Ethernet applications, including the emerging 100G (or Gigabits per second), 200G, 400G, 800G and the emerging 1.6T (or Terabits per second) port markets. Credo products are based on our proprietary Serializer/Deserializer (SerDes) and Digital Signal Processor (DSP) technologies. Our product families include Integrated Circuits (ICs) for the optical and line card markets, Active Electrical Cables (AECs) and SerDes Chiplets. Our intellectual property (IP) solutions consist primarily of SerDes IP licensing.

For more information, please visit **<https://www.credosemi.com>**. Follow Credo on **LinkedIn**.

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