



NEWS RELEASE

Credo Showcasing AEC PCIe Products for AI Scaling at NVIDIA GTC 2025

3/12/2025

SAN JOSE, Calif.--(BUSINESS WIRE)-- **Credo Technology Group Holding Ltd** (Credo) (NASDAQ: CRDO), an innovator in providing secure, high-speed connectivity solutions that deliver improved reliability and energy efficiency, is excited to announce its participation in NVIDIA GTC 2025, taking place March 17-21 in San Jose, CA. Credo will showcase its Toucan PCI Express (PCIe) Gen6.x/CXL3.x retimers in a full rack demo, highlighting their role in enabling scalable AI infrastructure. In addition, Credo's Associate Vice President of PCIe Product, Phil Kumin will deliver a technical session on GPU disaggregation and PCIe scaling for AI workloads.

Credo at NVIDIA GTC 2025 (Graphic: Business Wire)

Conference: NVIDIA GTC 2025

Date: March 17-21, 2025

Location: San Jose Convention Center, San Jose, CA

Booth: 1810

Booth Highlights:

- PCIe AECs – OSFP-XD PCIe AECs enabling flexible and energy efficient AI cluster scale-up.
- HiWire ZeroFlap (ZF) AECs for Ethernet – 800G 7m Host to Rail Scaleout AECs
- Interoperability – See Credo's PCIe AECs in a scale-up demo with XConn's 256-lane PCIe switch and 10 H100s
- Large Language Model (LLM) – See the live demo of a Hugging Face Llama 3.1 inference demo running on the cluster

Credo Presentation at NVIDIA GTC

Credo's Associate Vice President of PCIe Product, Phil Kumin, will present "Extending PCIe/CXL by Using Active

Electrical Cables" at NVIDIA GTC. This session will explore the first-of-its-kind disaggregated rack-scale GPU cluster, featuring an x86 server, ten NVIDIA H100 GPUs, a 256-lane PCIe 5.0 switch, and 512Mb PCIe 5.0 HiWire AECs running a live Llama 3.1 inference model. Attendees will gain real-world insights into AI deployment, hardware architecture, performance bottlenecks, and optimization opportunities for scaling AI clusters. The presentation will highlight how Credo, NVIDIA, and XConn are redefining AI infrastructure with PCIe AECs, a critical component for high-performance computing.

Credo invites all GTC 2025 attendees to visit our booth and attend Phil Kumin's session to learn how Credo's connectivity solutions are enabling the future of AI infrastructure.

To learn more about the Credo products in this release go to the product pages linked [here](#).

About Credo

Credo's mission is to redefine high-speed connectivity by delivering breakthrough solutions that enable the next generation of AI-driven applications. We are committed to enabling faster, more reliable, more energy-efficient, and scalable solutions that support the ever-expanding demands of AI, cloud computing, and hyperscale networks. Our innovations ease system bandwidth bottlenecks while simultaneously improving on power, security, and reliability. Our connectivity solutions are optimized for optical and electrical Ethernet applications, including the emerging 100G (or Gigabits per second), 200G, 400G, 800G and the emerging 1.6T (or Terabits per second) port markets. Credo products are based on our proprietary Serializer/Deserializer (SerDes) and Digital Signal Processor (DSP) technologies. Our product families include Integrated Circuits (ICs) for the optical and line card markets, Active Electrical Cables (AECs) and SerDes Chiplets. Our intellectual property (IP) solutions consist primarily of SerDes IP licensing.

For more information, please visit <https://www.credosemi.com>. Follow Credo on [LinkedIn](#).

Credo, the Credo logo and HiWire are registered trademarks of Credo Technology Group Limited in the United States and other jurisdictions. All other trademarks referenced herein are the property of their respective owners.

Media Contact:

Diane Vanasse

diane.vanasse@credosemi.com

Investor Contact :

Dan O'Neil

dan.oneil@credosemi.com

Source: Credo