Credo 16-nm 28G and 56G PAM-4 SerDes Now Available on TSMC FinFET Compact Process

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Enables Customers to Leverage Advanced, Low-Power Process for Next-Generation Designs

Milpitas, Calif., May 9, 2016 – Credo Semiconductor, a global innovation leader in Serializer-Deserializer (SerDes) technology, today announced the availability of its 28G and 56G PAM-4 SerDes transceiver IP on TSMC's 16-nm FinFET Compact (16FFC) process, enabling its growing customer base to take advantage of the low-power benefits of this advanced, new process in next-generation designs.

"Our close relationship with TSMC translates into a win-win for customers," said Bill Brennan, CEO of Credo Semiconductor. "As one of the first SerDes IP suppliers to port to this node, we enable the best of both worlds – the combination of robust SerDes solutions with TSMC's advanced low-power 16FFC process. We look forward to supporting the needs of our mutual customers who are already adopting 16FFC for their next-generation designs."

"TSMC continues to expand 16-nm manufacturing options, providing customers with advanced technology to enable their end products," said Suk Lee, TSMC Senior Director, Design Infrastructure Marketing Division. "We have many customers that want both 28G and 56G SerDes IP solutions, and the availability of Credo's IP provides a proven solution for 16FFC high-performance computing applications."

AVAILABILITY AND DELIVERABLES

The Credo 28G and 56G PAM4 SerDes IP is available now on the TSMC 16-nm FinFET Plus (FF+) and 16FFC processes. Deliverables include user and SoC integration guides; netlist; timing library; register map; Verilog, ATPG, and IBIS-AMI models; LEF views; Layout Versus Schematic (LVS) and Design Rule Check (DRC) reports.

Those interested in learning more about the company's current silicon and intellectual property engagement options, as well as future developments, should contact sales@credosemi.com.

ABOUT CREDO SEMICONDUCTOR

Credo is a leading provider of high performance, mixed-signal semiconductor solutions for the data center, enterprise networking and high performance computing markets. Credo's advanced Serializer-Deserializer (SerDes) technology delivers the bandwidth scalability and end-to-end signal integrity for next generation platforms requiring single-lane 25G, 50G, and 100G connectivity. The company makes its SerDes available in the form of Intellectual Property (IP) licensing on the most advanced processing nodes and with complementary product families focused on extending reach and multiplexing to higher data rates. Credo is headquartered in Milpitas, California and has offices in Shanghai and Hong Kong. For more information: www.credosemi.com.