Credo Delivers Industry's Lowest Power 16nmFF+ 28GLR-Compliant SerDes IP with Comprehensive Development Platform

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Speeds Deployment of Chips and Systems for Next Generation Data Center Designs

Milpitas, Calif., December2, 2015 – Credo Semiconductor, a global innovation leader in Serializer-Deserializer (SerDes) technology, today announcedthe commercial availability of a complete development platform for its advanced 28G SerDes IP on the TSMC16nmFF+ process node.The IP has been proven to deliver the industry's lowest power, consuming less than200mW per transceiver at long reach (LR), and can be immediately deployed in chips and systems supporting a variety of industry standards such as IEEE 802.3bj and OIF CEI-25G-LR.

"By shattering perceived barriers in performance, power and process scalability, our SerDes IP enables SOC designers to rethink their design approach – the key factor in enabling true product innovation," said Jeff Twombly, vice president of Business Development for Credo. "Interconnect speeds in the data center are quickly transitioning to 25G, and Credo is well-positioned to enable the necessary SOC innovation at the 16nmFF+ processnode with a comprehensive development platform."

Designers use Credo's SerDes technology to accelerate throughput and deliver end-to-end signal integrity in next-generation platforms requiring single-lane 25G, 50G, and 100G connectivity, such as next-generation switches and routers for 100G and 400G enabled networks.

Credo's 28G SerDes development platform features:

- Characterized silicon results on TSMC 16nmFF+
- Less than 200mW dissipation per SerDes transceiver
- Self-contained evaluation board with a robust GUI
- Advanced and flexible equalization capabilities with>35dB of insertion loss performance at PRBS31
- Excellent random jitter performance of<150fs
- Complete diagnostic suite
- Broad standards support
- Support for a broad range of data rates from 1.25G to 28.25G
- Comprehensive documentation, modeling, layout and verification information

AVAILABILITY AND PACKAGING

The Credo 28G SerDes IP is available now on both TSMC 16nmFF+ and TSMC28-nm process nodes. Deliverables include User and SoC integration guides; netlist; timing library; register map; Verilog, ATPG, and IBIS-AMI models; LEF views; Layout Versus Schematic (LVS) and Design Rule Check (DRC) reports.

Companies interested in learning more about the company's current silicon and intellectual property engagement options, as well as future developments, should contact sales@credosemi.com.

ABOUT CREDO SEMICONDUCTOR

Credo is a leading provider of high performance, mixed-signal semiconductor solutions for the data center, enterprise networking and high performance computing markets. Credo's advanced Serializer-Deserializer (SerDes) technology delivers the bandwidth scalability and end-to-end signal integrity for next generation platforms requiring single-lane 25G, 50G, and 100G connectivity. The company makes its SerDes available in the form of Intellectual Property (IP) licensing on the most advanced processing nodes and with complementary product families focused on extending reach and multiplexing to higher data rates. Credo is headquartered in Milpitas, California and has offices in Shanghai and Hong Kong. For more information: www.credosemi.com.